

REMARKS

Claims 1-60 are pending in this application with claims 1, 11, 21, 33, 46, 50 and 54 being independent. Claims 1, 11, 21, 46, 47, 50, 51, 54 and 55 have been amended.

The Examiner has objected to the form of claim 21. Applicant has amended claim 21 in accordance with the Examiner's suggestion.

Claims 51 and 55 have been rejected as being indefinite. Claims 51 and 55 have been amended to address the Examiner's concerns.

Independent claim 1 and dependent claims 2, 4 and 5 have been rejected as being anticipated by Parks (U.S. Patent No. 5,471,225), and dependent claims 9 and 10 have been rejected as being unpatentable over Parks. Claim 1, as amended, recites a liquid crystal device including "a write-in storage circuit selection portion" and "a read storage circuit selection portion." Applicant requests reconsideration and withdrawal of the rejection of claim 1 and its dependent claims because Parks does not describe or suggest at least the recited read and write storage circuit selection portions. Rather, Parks describes a liquid crystal cell 48 which buffers voltage levels on a liquid crystal display electrode 36 using a pass-gate transistor 38 and a latching circuit that is placed between the pass-gate transistor and the display electrode 36. This buffering circuit of Parks does not describe or suggest the recited read and write storage circuit selection portions.

Dependent claims 3 and 6-8 which depend from claim 1, have been rejected as being unpatentable over Parks in view of Yamazaki (U.S. Patent No. 5,349,366), Parks in view of Fonash (U.S. Patent No. 5,945,866), Parks in view of Johnson (U.S. Patent No. 4,752,118), and Parks in view of Kobayashi (U.S. Patent No. 4,432,610), respectively. For at least the reasons discussed above, Parks does not describe or suggest the read and write storage circuit selection portions included in claims 3 and 6-8 through their dependency from claim 1. Yamazaki, Fonash, Johnson, and Kobayashi do not remedy the deficiencies of Park. Therefore, applicants request reconsideration and withdrawal of the rejection of claims 3 and 6-8.

Independent claim 11 and dependent claims 12 and 58 have been rejected as being anticipated by Kanaly (U.S. Patent No. 5,225,823) and dependent claims 19 and 20 have been

rejected as being unpatentable over Kanaly. Claim 11, as amended, recites a liquid crystal display device including "a write-in storage circuit selection portion," "a read storage circuit selection portion," "a write-in transistor electrically connected to the write-in storage circuit selection portion," and "a read transistor electrically connected to the read storage circuit selection portion." Applicant requests reconsideration and withdrawal of the rejection of claim 11 and its dependent claims because Kanaly does not describe or suggest the recited read and write storage circuit selection portions and read and write transistors. Rather, Kanaly describes a liquid crystal cell 19 that includes switches 30 and 32 that operate to store a charge on a storage capacitor 24 during a data loading phase and then subsequently place the charge on a main cell capacitance 28 during a display or power phase.

Dependent claims 13 and 16-18, which depend from claim 11, have been rejected as being unpatentable over Kanaly in view of Yamazaki, Kanaly in view of Fonash, Kanaly in view of Johnson, and Kanaly in view of Kobayashi, respectively. For at least the reasons discussed above, Kanaly does not describe or suggest the recited read and write storage circuit selection portions and read and write transistors included in claims 13 and 16-18 through their dependency from claim 11. Yamazaki, Fonash, Johnson, and Kobayashi do not remedy the deficiencies of Kanaly. Therefore, applicants request reconsideration and withdrawal of the rejection of claims 13 and 16-18.

Independent claims 46, 50, and 54 and dependent claims 48-50, 52, and 56 have been rejected as being unpatentable over Koyama (U.S. Patent No. 5,798,746) in view of Kanaly. Claims 46, 50, and 54, as amended, recite a method of driving a liquid crystal display device including "writing the n bit digital image signal into the storage circuit via a write-in transistor" and "performing the n bit digital image signal read from the storage circuit via a read transistor." Applicant requests reconsideration and withdrawal of the rejection of claims 46, 50, and 54, and their dependent claims, because neither Kanaly, Koyama, nor any combination of the two describes or suggests the recited writing and performing operations.

Dependent claims 47, 51, and 55, which depend from claims 46, 50, and 54, respectively, have been rejected as being unpatentable over Koyama in view of Kanaly and Okumura (U.S.

Patent No. 5,945,972). Okumura does not overcome the deficiencies of Kanaly and Koyama. Accordingly, neither Kanaly, Koyama, Okumura, nor any combination of the three describes or suggests the recited writing and performing operations. For at least this reasons, applicants request reconsideration and withdrawal of the rejection of claims 47, 51, and 55.

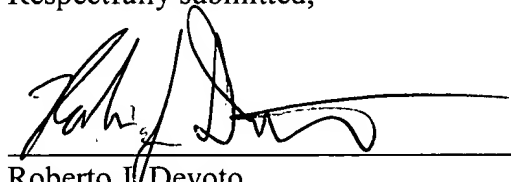
Independent claims 21 and 33 and dependent claims 22-32, 34-45, and 59-60 have been provisionally rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 3-5 of copending Application No. 09/912,596. Since this is a provisional rejection, applicants request that this rejection be held in abeyance until such time as the claims of the '596 application are allowed and the present claims are otherwise found to be allowable.

Enclosed is a \$110 check for the Petition for Extension of Time fee. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: _____

8/2/04



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